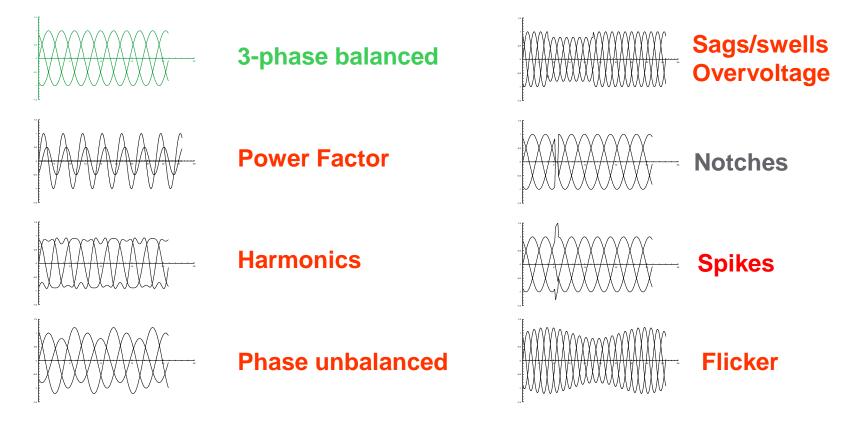


Power Quality & Harmonic Mitigating Solutions

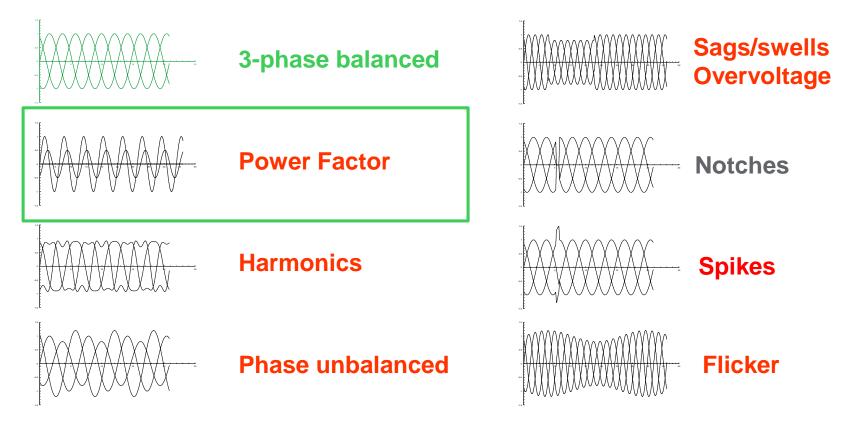
IEEE CED Seminar, Houston



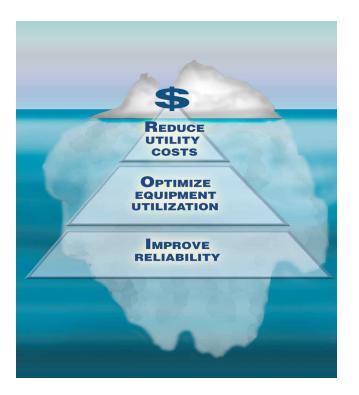
The ideal voltage supply does not exist



The ideal voltage supply does not exist



Energy Efficiency with PFC

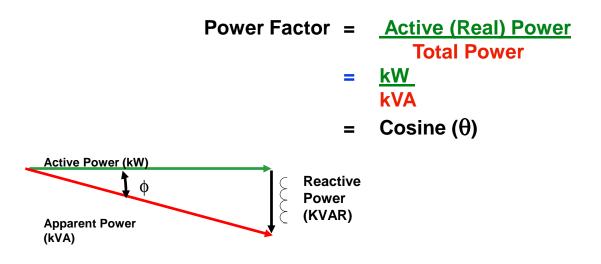


Power Factor Correction benefits:

- Reduces Utility Bills
- Reduces loading on transformers
- Reduces I2R losses in Distribution Equipment
- Reduces Carbon Emission
- Reduces voltage drop

The Power Triangle:

Power Factor is the ratio of Active Power to Total Power:

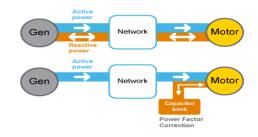


Waters of the second seco

Inductive loads that cause low PF:

- Induction motors
- Welders
- DC drives
- Transformers
- ...

Power Factor is a measure of efficiency (Output/Input)

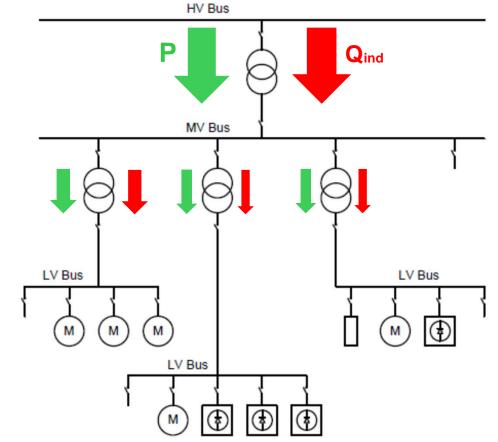


Power Factor Active (Real) Power = Active Power 100kW **Apparent Power** θ Reactive 100 kW = Power, 125 kVA Inductive **Apparent Power 125kVA** Cosine $(\theta) = .80$ = .95 pf 100 kW **Active Power 100kW** = *kVA* Add kVA 100 kW = 42 KVARc .95 Apparent Power 105 kVA corrects PF Apparent to 95% lag **Power** 105 kVA =

KVAR = KW x (Tan Cos⁻¹(Present PF) - Tan Cos⁻¹(Desired PF))

How to correct Power Factor?

Power flow in MV and LV network without compensation



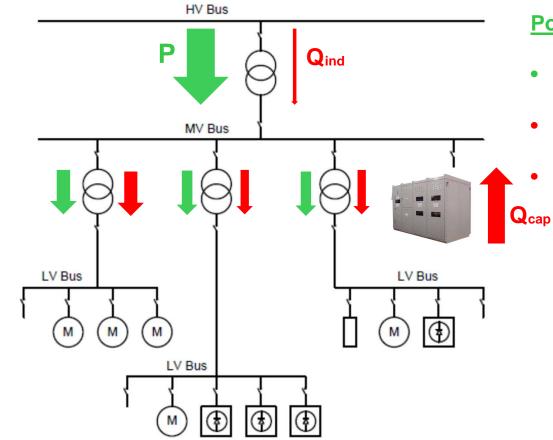
Power Flow:

- Real Power supplied by HV, MV & LV network
- Reactive Power supplied by HV, MV & LV network

Impact:

- PF penalty possible
- No voltage regulation
- No I²R losses or CO² reduction
- No LV or MV network off loading

Power flow in MV and LV network with MV compensation



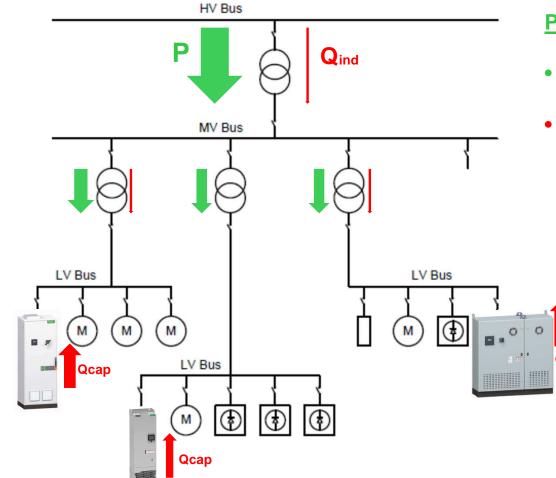
Power Flow:

- Real Power supplied by HV, MV & LV network
- Reactive Power supplied by MV PFC and some by HV network
- Full Reactive Power still flow through the LV network

Impact:

- PF penalty eliminated
- No LV voltage regulation
- Some I²R & CO² reduction
- MV network off loading, no LV network off loading

Power flow in MV and LV network with LV compensation



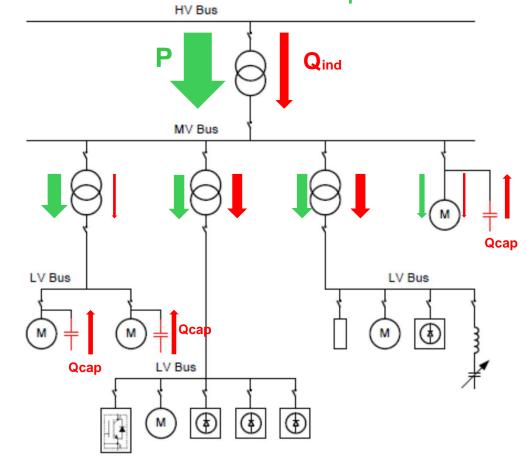
Power Flow:

- Real Power supplied by HV, MV & LV network
- Reactive Power supplied by LV PFC & AccuSine. Small amount supplied by the network

Impact:

- PF penalty eliminated
- Voltage regulation
- Uptimum I²R & CO²
 reduction
- Qcap LV & MV network off loading
 - Harmonic reduction by De-Tuned LV PFC and AccuSine PLUS

Fixed Reactive Power Compensation



Application :

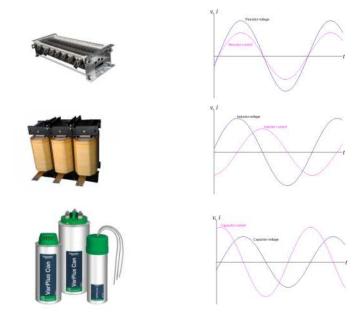
- Fixed capacitor can be used in network with low harmonic distortion level.
- When installed on the main bus, keep fixed capacitor kVAR value below <u>15%</u>
 <u>of transformer kVA rating</u> in order to avoid over voltage condition.
- When installed on motors only apply on DOL starter.
- VFD's not allowed on the same bus.
- When reduced voltage starter are present, only energized the Fixed capacitor when motor has reached full speed.
- Back to back capacitor switching issue possible when multiple fixed capacitor are present on the same bus.

Power Factor Correction for Linear Loads

Linear loads

• The electrical equipment draws current in a "linear" fashion

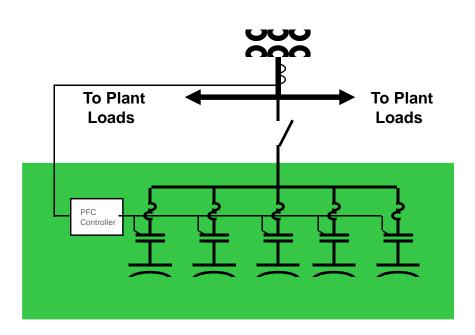
Current (i) & Voltage (v) are both "Sinusoidal"



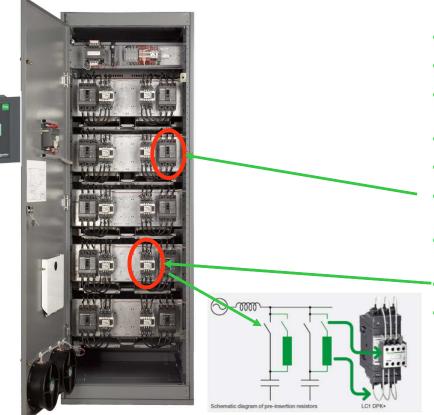
Automatic Capacitor Systems

Automatic Capacitor Systems:

- Contains:
 - PFC Controller
 - Stage over current protection, Fuses or Circuit Breaker
 - Contactors
 - Capacitors
- Usually at Main Swith Gear
- Controller Measures P.F. & switches banks in & out of service to maintain user defined target P.F.



LV Automatic Capacitor Bank for Industries



Automatic capacitor bank NEMA 1, Indoor

- Standard Voltage, 208,240, 480 & 600 V, 50-60 Hz
- Free Standing, Main Lugs or Main Breaker incoming
- Up to 500 KVAR @ 480 or 600 V in each section, 1000 KVAR max in two sections
- Section dim: 30" W x 36"D x 90" H
- PFC relay, Advanced Microprocessor controller
- Stage Circuit Breaker used for overload and over current protection
- cCSAul Approved, Optimized air flow & dead front construction
- Contactors equipped with soft charge resistors
- Heavy Duty LV capacitor, certified as per UL810



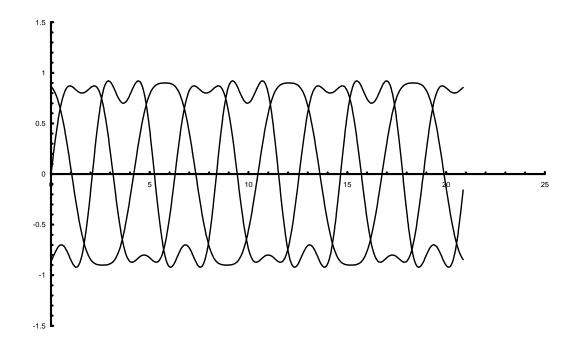
LV Automatic Capacitor Bank for commercial buildings and small industries



Automatic capacitor bank NEMA 1, Indoor

- Standard Voltage, 480 & 600 V, 50-60 Hz
- Wall Mounted, Main Lugs or Main Breaker incoming
- Up to 300 KVAR @ 480 and 250 kVAR at 600 V
- Small enclosure: 31.5" W x 16"D x 33.5" H
- Large Enclosure: 39.4" W x 16"D x 47" H
- PFC relay, Advanced Microprocessor controller
- Stage Circuit Breaker used for overload and over current protection
- cCSAul Approved, Optimized air flow & dead front construction
- Contactors equipped with soft charge resistors
- Heavy Duty LV capacitor, certified as per UL810

Introduction to Harmonics and PF correction in harmonic rich network



Harmonics in electrical systems increase business operating costs.....

Increased system downtime

- Nuisance tripping of overloads and circuit breakers
- Bus failures
- Distortion of control signals

Increased maintenance

• Excessive heat places burden on electrical infrastructure from transformers to cables and bussing

Lower Quality and Efficiency

Interrupt production causing downtime, rework and scrap

Reduced system capacity

Requires costly equipment upgrades to support expansion

Harmonics are a circumstance of progress and they effect almost every business in today's environment...



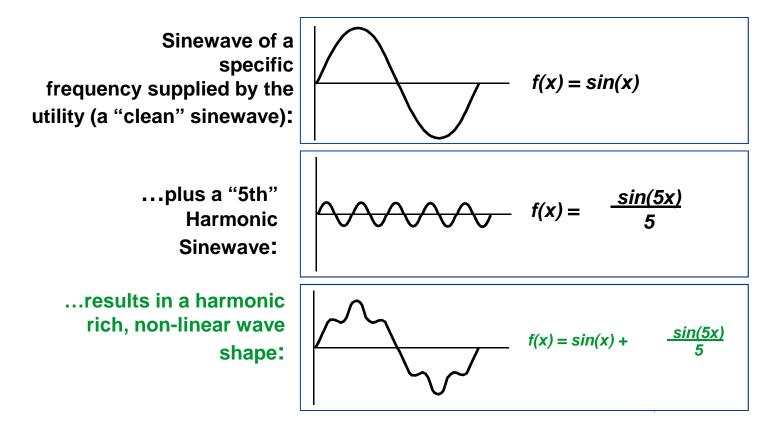
Above thermal and daylight images show a three phase motor which has overheated. Power quality analysis proved condition was caused by negative sequence harmonics.



Above thermal image shows overheated windings on a step-down transformer, possibly caused by harmonics.

Definition:

Harmonics are integer multiples of the fundamental frequency that, when added together, result in a distorted waveform



What produces "Non-linear" Current?

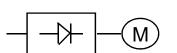
Harmonics: Fundamentals



Computers

• Copiers

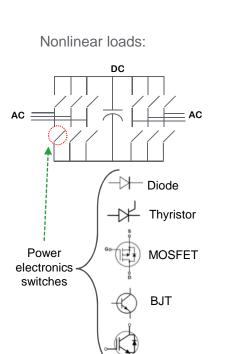




• AC or DC drives



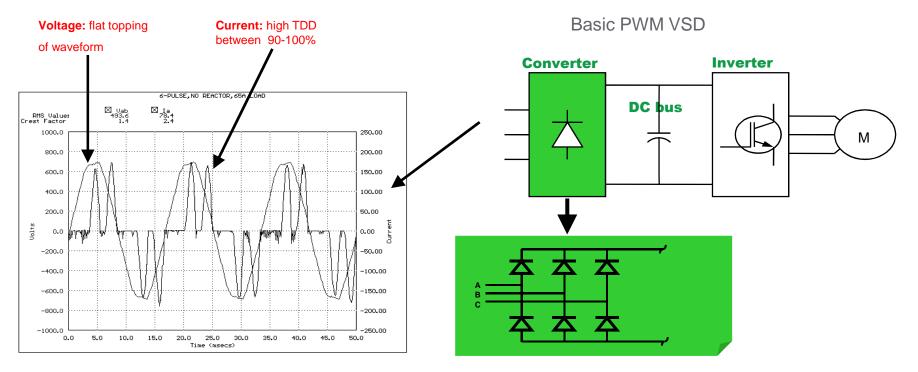
 Electronic Ballasts



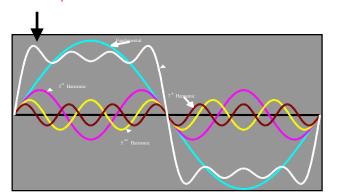
Type of Load	Typical Waveform	Current Distortion
Single Phase Power Supply		80% (high 3rd)
Semiconverter		high 2nd,3rd 4th at partial loads
6 Pulse Converter, capacitive smoothing, no series inductance		80%
6 Pulse Converter, capacitive smoothing with series inductance > 3%, or dc drive	MWW	40%
6 Pulse Converter with large inductor for current smoothing		28%
12 Pulse Converter		15%
ac Voltage Regulator		varies with firing angle
Fluorescent Lighting		20%

• Nonlinear loads draw harmonic current from source

• Does no work



Waveform seen with oscilloscope



<u>Harmonic</u>	Frequenc	<u>ySequence</u>
1	60Hz	+
2	120Hz	-
3	180Hz	0
4	240Hz	+
5	300Hz	-
6	360Hz	0
7	420Hz	+
:	:	
19	1140Hz	+

 Characteristic harmonics are the predominate harmonics seen by the power distribution system

• Predicted by the following equation:

 $H_c = np \pm 1$

- H_C = characteristic harmonics to be expected
- n = an integer from 1,2,3,4,5, etc.
- p = number of pulses or rectifiers in circuit
- Amplitude is inverse of harmonic order (perfect world)

Multi-pulse converter

Harmonic signature

	Harmonics present by rectifier design					
	Type of rectifier					
	1 phase	2 phase	3 phase	3 phase	ase 3 phase	
Hn	4-pulse	4-pulse	6-pulse	12-pulse	18-pulse	
3	x	x				
5	x	x	×			
7	x	x	×			
9	x	x				
11	x	x	×	x		
13	x	x	×	x		
15	x	x				
17	x	x	×		x	
19	x	x	x		x	
21	x	x				
23	x	x	x	x		
25	x	x	x	x		
27	x	x				
29	х	x	x			
31	x	x	x			
33	x	x				
35	х	х	х	х	x	
37	х	х	x	х	x	
39	х	х				
41	х	х	х			
43	х	х	х			
45	х	х				
47	х	х	х	х		
49	×	×	×	×		

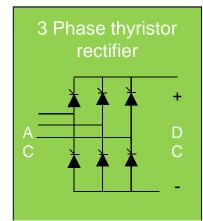
$H_{c} = np + - 1$

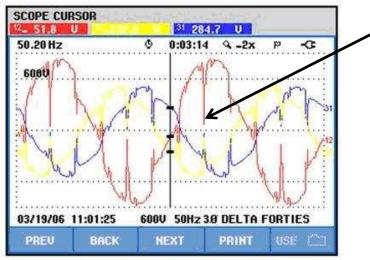
 H_c = characteristic harmonic order present

n = an integer

p = number of pulses

Multi-pulsing (ie: 12 & 18 pulses): Elimination of lower order harmonic removes largest amplitude harmonics





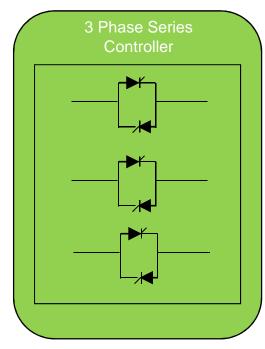
3 Phase thyristor rectifier (parallel, phase to phase)

Converts **AC** to controlled **DC** Max harmonics at full load Best PF at full load

Harmful characteristic

- Causes voltage notching (THDv)
- > Requires input line reactors (inductance) to reduce notch depth

Notch created by a momentary short circuit when SCR commute from one phase to the other



Transitions are short duration (2-3 seconds) PF according to AC motor design

3 Phase controller (series)

Opposing (anti-parallel) thyristors per phase (not a rectifier)

AC to AC (variable volts) No harmonics at full output

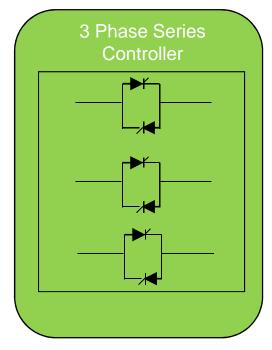
No harmonics at full output PF is load dependent i.e. AC Motor

Solid State Starters (SSS)

Transition harmonics only During acceleration and deceleration

- Transition lagging PF
 - At full voltage AC motor characteristics apply
 - Thyristors are full ON or Bypass contactor used to bypass

No snubbers (R-C) on thyristors



Harmonics and PF increase and decrease together

Resistive & Inductive Heaters

Same thyristor configuration as SSS Different use as compared to SSS

- Designed to control current through resistor banks or inductive coils to control heating
- <u>High harmonics</u> except at full load
- <u>**Poor** PF</u> except at full load

IEEE 519-2014

IEEE 519-2014

IEEE Recommended Practice and Requirements for Harmonic Control in Electric Power Systems

%TDD limits on users

Harmonic voltage distortion limits are provided to reduce the potential negative effects on user and system equipment. Maintaining harmonic voltages below these levels necessitates that

- All users limit their harmonic current emissions to reasonable values determined in an equitable manner based on the inherent ownership stake each user has in the supply system and
- Each system owner or operator takes action to decrease voltage distortion levels by modifying the supply system impedance characteristics as necessary.

%THDv limits on suppliers

IEEE 519-2014

Note: THDi is not used in IEEE 519-2014

Harmonic distortion terms used

total demand distortion (TDD): The ratio of the root mean square of the harmonic content, considering harmonic components up to the 50th order and specifically excluding interharmonics, expressed as a percent of the maximum demand current. Harmonic components of order greater than 50 may be included when necessary.

total harmonic distortion (THD): The ratio of the root mean square of the harmonic content, considering harmonic components up to the 50th order and specifically excluding interharmonics, expressed as a percent of the fundamental. Harmonic components of order greater than 50 may be included when necessary.

The recommended limits in this clause apply only at the point of common coupling and should not be applied to either individual pieces of equipment or at locations within a user's facility. In most cases, harmonic voltages and currents at these locations could be found to be significantly greater than the limits recommended at the PCC due to the lack of diversity, cancellation, and other phenomena that tend to reduce the combined effects of multiple harmonic sources to levels below their algebraic summation.

IEEE 519-2014

Supplier standard for THDv

New category for <1.0 kV (applies at 480 & 600 VAC)

Bus voltage V at PCC	Individual harmonic (%)	Total harmonic distortion THD (%)
$V \le 1.0 \text{ kV}$	5.0	8.0
$1 \text{ kV} \le V \le 69 \text{ kV}$	3.0	5.0
$69 \text{ kV} < V \le 161 \text{ kV}$	1.5	2.5
161 kV < V	1.0	1.5 [*]

New voltage class

Table 1—Voltage distortion limits

Limited to 50th order

IEEE 519-2014

USER standard for

TDD limits

Same as 519-1992

Table 2—Current distortion limits for systems rated 120 V through 69 kV

			armonic curr n percent of <i>l</i>			
	Indi	ividual harm	onic order (o	dd harmonics	s) ^{n, b}	
$I_{\rm SC}/I_{\rm L}$	$3 \le h < 11$	$11 \le h \le 17$	$17 \le h \le 23$	$23 \le h < 35$	$35 \le h \le 50$	TDD
< 20 ^c	4.0	2.0	1.5	0.6	0.3	5.0
20 < 50	7.0	3.5	2,5	1.0	0.5	8.0
50 < 100	10.0	4.5	4.0	1.5	0.7	12.0
100 < 1000	12.0	5.5	5.0	2.0	1.0	15.0
>1000	15.0	7.0	6.0	2.5	1.4	20.0

^aEven harmonics are limited to 25% of the odd harmonic limits above.

^bCurrent distortions that result in a dc offset, e.g., half-wave converters, are not allowed.

^cAll power generation equipment is limited to these values of current distortion, regardless of actual I_{sc}/I_{L} .

where

 I_{sc} = maximum short-circuit current at PCC

 $I_{\rm L}$ = maximum demand load current (fundamental frequency component)

at the PCC under normal load operating conditions

TDD versus THD(I)

•TDD and THD(I) are not the same except at 100% load

Example: with AccuSine PCS+ operating

	Measured				
	Total I,	Fund I,	Harm I,		
	rms	rms	rms	THD(I)	TDD
Full load	▶ 936.68	936.00	35.57	3.8%	3.8%
_	836.70	836.00	34.28	4.1%	3.7%
	767.68	767.00	32.21	4.2%	3.4%
creases,	592.63	592.00	27.23	4.6%	2.9%
	424.53	424.00	21.20	5.0%	2.3%
ases while	246.58	246.00	16.97	6.9%	1.8%
reases.	111.80	111.00	13.32	12.0%	1.4%

As load decreases, TDD decreases while THD(I) increases.

How Harmonics Affect Capacitors:

Capacitors are naturally a low impedance to high frequencies:

• Caps absorb harmonic in current

As capacitor absorbs harmonic in current, the capacitor heats up

Reduced life expectancy

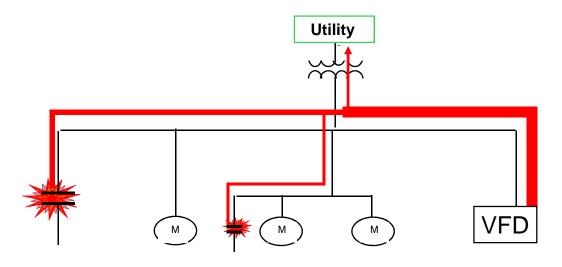
Voltage harmonics stress the capacitor dielectric

Reduced life expectancy

Parallel combination of capacitors with motor or transformer can cause resonance......

Capacitors Absorb Harmonic in current

The capacitor has lower impedance than the utility, therefore it absorbs the harmonics

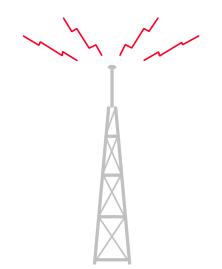


capacitor diverts flow of harmonics Harmonic current increases capacitor absorbes harmonic current capacitor overheats & can fail over time

or worse.....

How Harmonics Affect Capacitors:

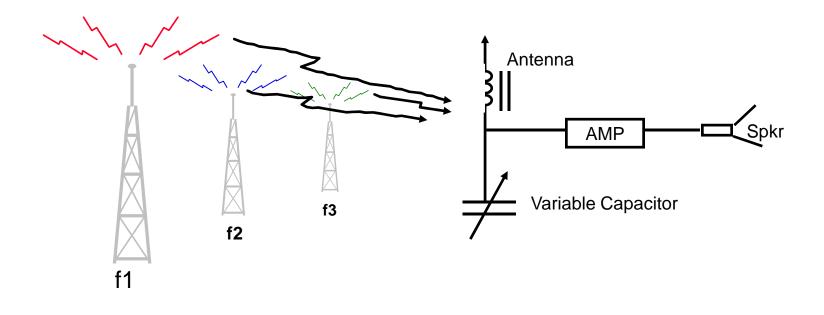
You use the principle of resonance every day!





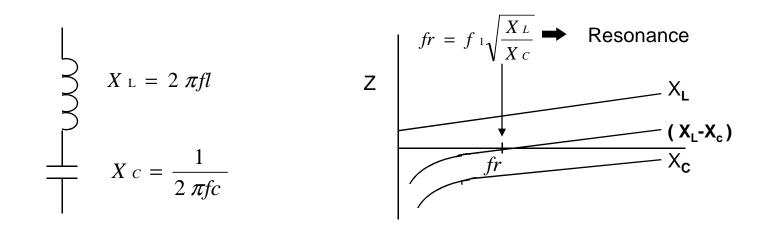
How Harmonics Affect Capacitors:

A Radio uses Resonance to Capture a Radio Station:



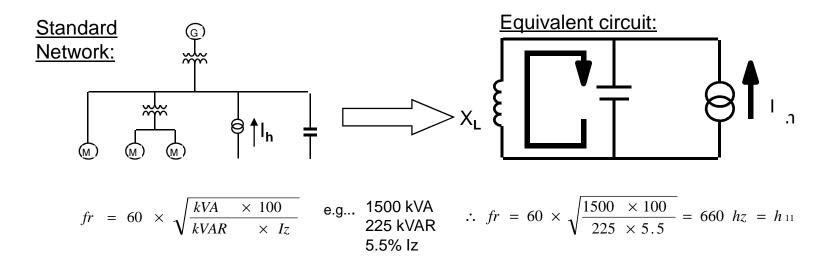
How Harmonics Affect Capacitors (Resonance)

Resonance:



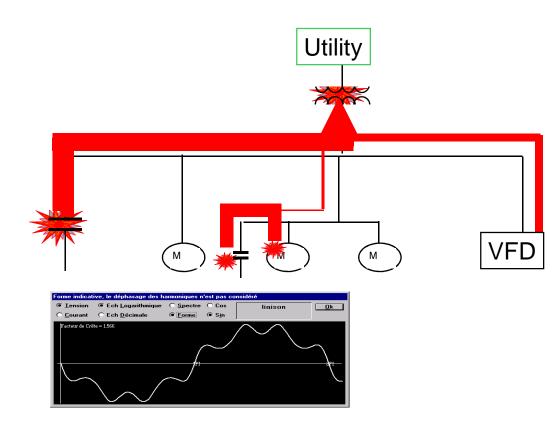
How Harmonics Affect Capacitors:

How Capacitors "Tune" a circuit:



Parallel Resonance and harmonic magnification

Resonance:



Amplification of current between capacitor and transformer

Current distortion rises

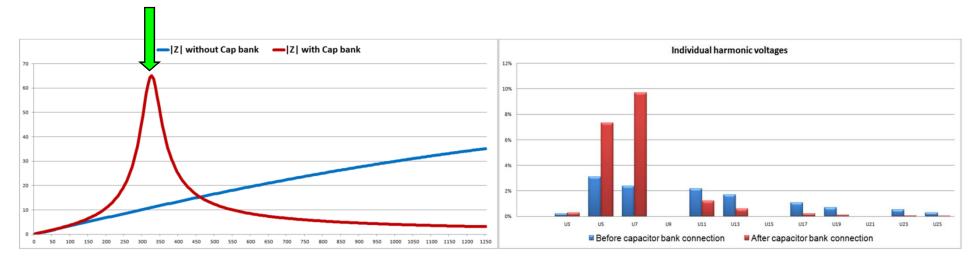
Voltage distortion rises

Main transformer &/or capacitor fuses blow

Equipment damage

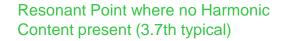
Parallel Resonance

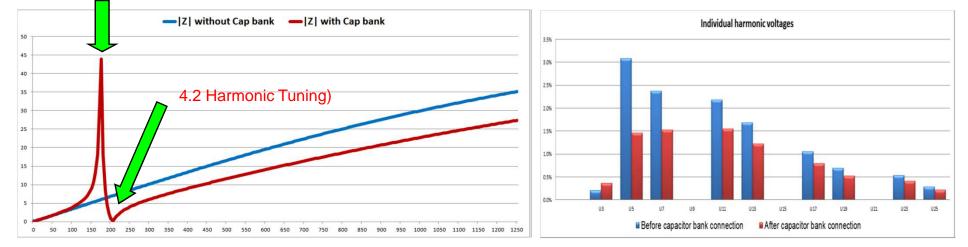
Resonant Point likely to amplify dominant harmonic (typically 5th, 7th and 11th)



Magnification of Harmonic Current and Voltage when Standard Capacitor are Added to the Network

De-Tune to Avoid Resonance



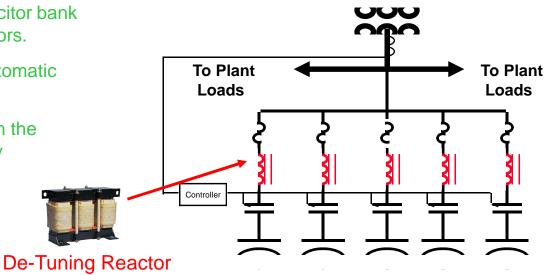


Effect on Harmonic Current and Voltage when De-Tuned Capacitor Bank is Applied (AV6000 & AT6000)

Low Voltage Automatic Capacitor Bank with De-tuning reactors

De-Tuned (DR) automatic capacitor bank :

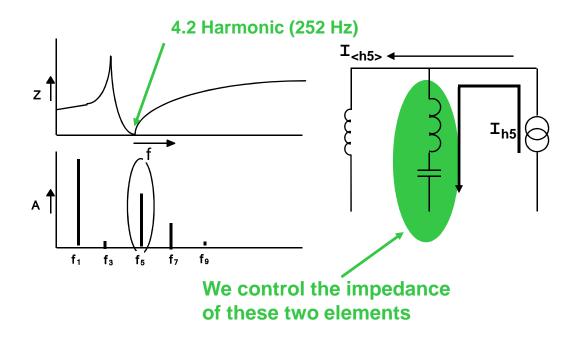
- Same as automatic capacitor bank with c/w De-Tuning reactors.
- Works like a standard automatic capacitor bank
- Avoid resonance between the capacitors and the supply transformer.



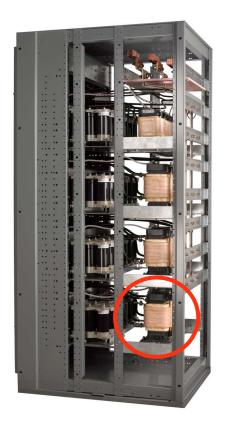
Power Factor Correction With Harmonics:

De-tuning a network:

• "Force" the resonant point away from naturally occurring harmonics



Low Voltage De-Tuned Automatic Capacitor Bank for Industries





Automatic capacitor bank NEMA 1, Indoor

- Standard Voltage, 208,240, 480 & 600 V, 50-60 Hz
- Free Standing, Main Lugs or Main Breaker incoming
- Up to 400 KVAR @ 480 or 600 V in each section, 1200 KVAR max in three sections
- Section dim: 30" W x 36"D x 90" H
- PFC relay, Advanced Microprocessor controller
- Stage Circuit Breaker used for overload and over current protection
- cCSAul Approved, Optimized air flow & dead front construction
- Contactors rated for capacitor switching
- Heavy Duty LV capacitor, certified as per UL810
- De-Tuning Reactors tuned to 252 Hz

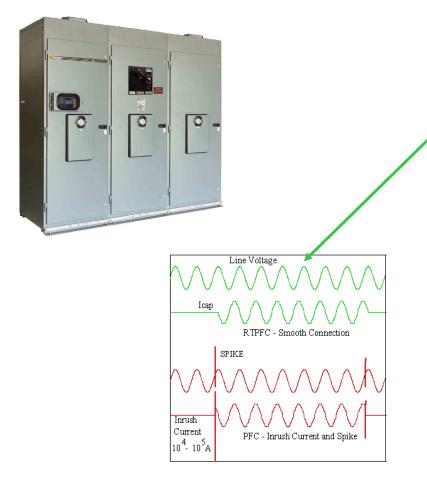
LV **De-Tuned** Automatic Capacitor Bank for commercial buildings and small industries



Automatic capacitor bank NEMA 1, Indoor

- Standard Voltage, 480 & 600 V, 50-60 Hz
- Floor or Wall Mount, Main Lugs or Main Breaker incoming
- Up to 200 KVAR @ 480 or 600 V
- Enclosure size: 31.5" W x 16"D x 33.5" H
- PFC relay, Advanced Microprocessor controller
- Stage Circuit Breaker used for overload and over current protection
- cCSAul Approved, Optimized air flow & dead front construction
- Contactors rated for capacitor switching
- VarPLus Can Heavy Duty LV capacitor, certified as per UL810
- De-Tuning Reactors tuned to 252 Hz

Transient Free Automatic Capacitor Banks



For sensitive networks

Similar to De-Tuned Capacitor Bank except it's equipped with solid state switching

Transient Free switching

Reactor tuned to 4.2 to 4.7

Response time of less than 5 sec

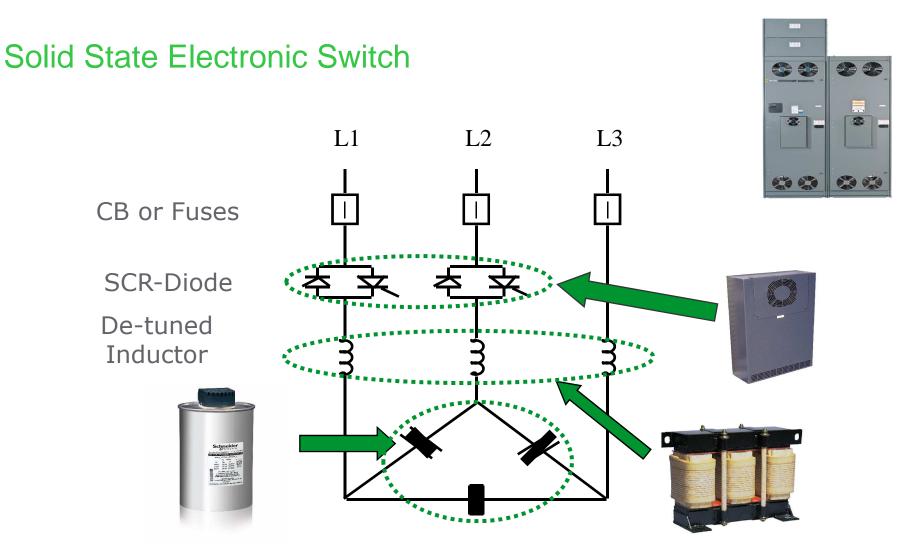
Up to 450kVAR per section

Expandable up to 1350kVAR without split incoming

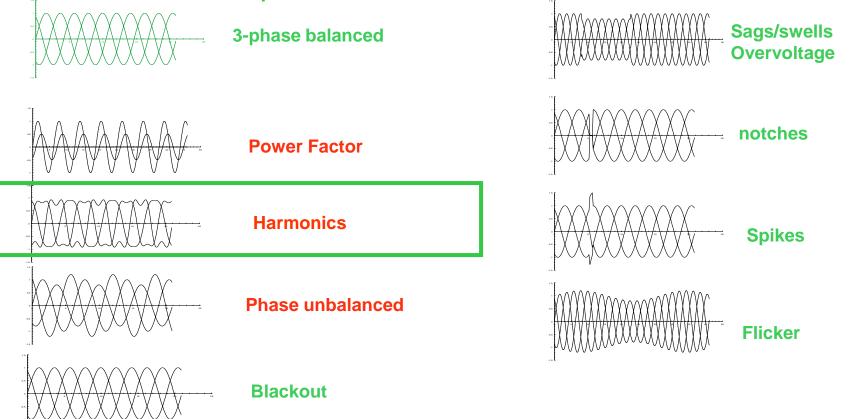
Larger systems available with split incoming

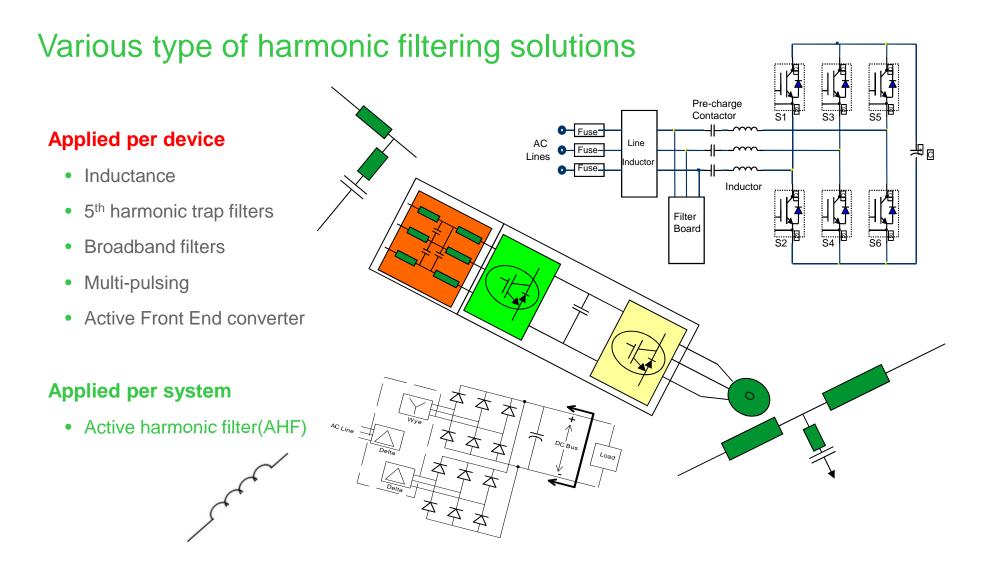
Additional cubicles can be field installed if required

cCSAul Approved

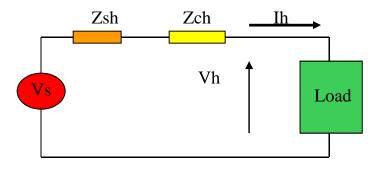


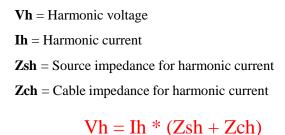
The ideal voltage supply does not exist, Active Harmonic Filters can correct 3 PQ problems





Harmonics: Fundamentals

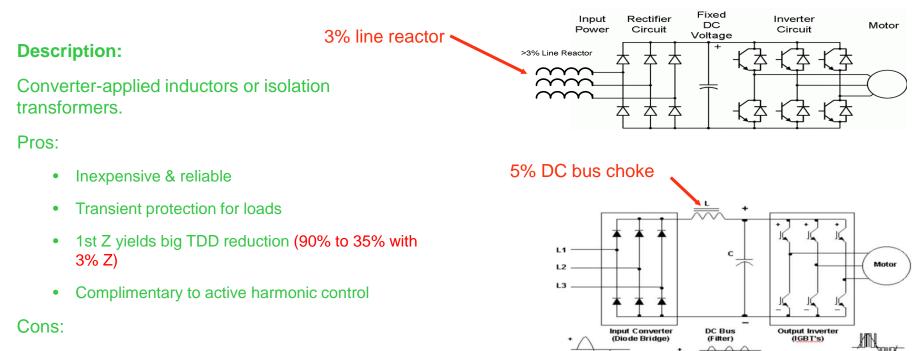




Harmonic voltages (Vn):

- Develop as the harmonic current traverses the electrical system.
- Each harmonic order has its own system impedance (Zn) and thus develops its own harmonic voltage.
- The root-mean-square (rms) of all harmonic orders equals the total amplitude of harmonic current or voltage.
- Ohm's Law applies: Vn = In * Zn
- To reduce Vh: Reduce system impedance (Zsh & Zch) or reduce current (Ih)

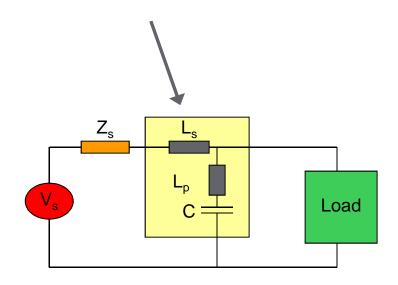
Inductors/Transformers/DC Bus Chokes



- Limited reduction of TDD at equipment terminals after 1st Z
- Reduction dependent on source Z

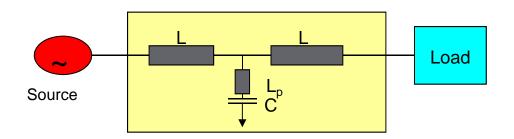
5th Harmonic Filter (Trap Filter)

- Inductor (L_p) and Capacitor (C) provide low impedance source for a single frequency (5th)
 - Must add more tuned filters to filter more frequencies
- \bullet Inductor L_{s} required to detune filter from electrical system and other filters
 - If L_s not present, filter is sink for all 5th harmonics in system, that can result in overlaod.
 - \bullet If L_{s} not present, resonance with other tuned filters possible
- Injects leading reactive current (KVAR) at all times may create leading PF and/or issues with back up generator



Broadband Filters

- Mitigates up to 13th order or higher
- Each inductor (L) > 8% impedance
 - V drops ~ 16% at load
 - Trapezoidal voltage to load
 - •Can only be used on diode converters
 - Prevents fast current changes (only good for centrifugal loads)
 - When generators are present, re-tuning may be required
- Capacitor (C) designed to boost V at load to proper level (injects leading VARs)
- Physically large
- High heat losses (>5%)
- Series device



Multi-Pulse Drives

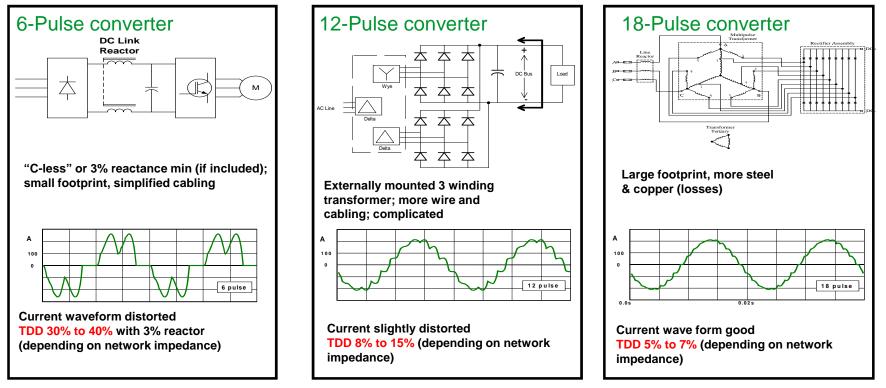
Description: Drives/UPS with two (12 pulse) or three (18 pulse) input bridges fed by a transformer with two or three phase shifted output windings.

•Pros:

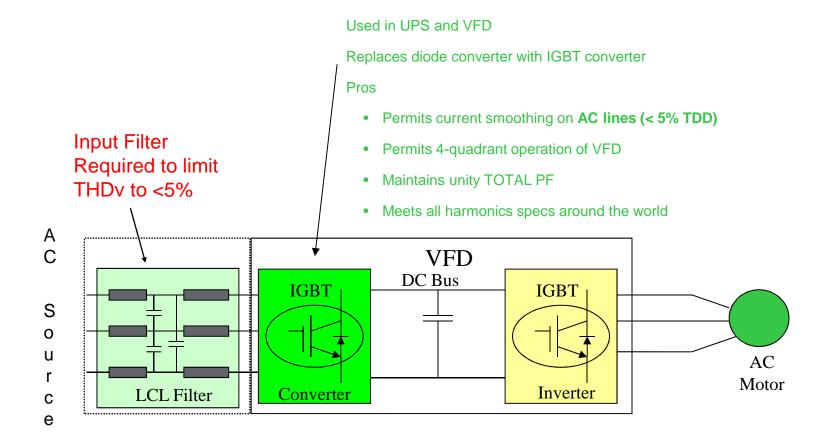
- Reduces TDD to 10% (12 pulse) & 5% (18 pulse) at loads
- Reliable
- •Cons:
 - High installation cost with external transformer
 - Large footprint (even w/autotransformer)
 - Series solution with reduction in efficiency
 - One required for each product
 - Cannot retrofit

Harmonic mitigation methods

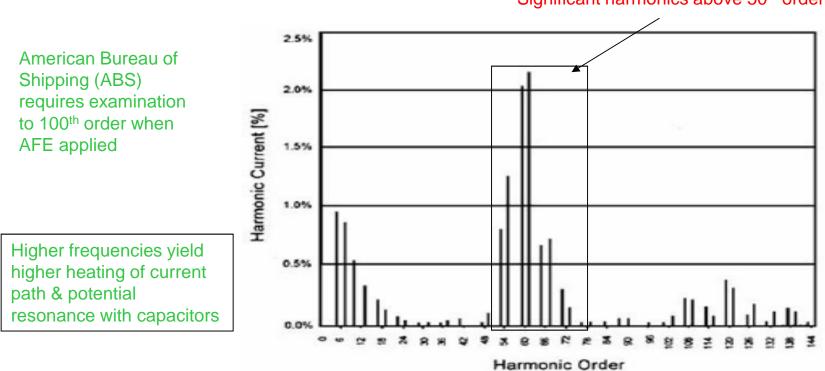
VFD mitigation topologies



Active Front End (AFE) Converters

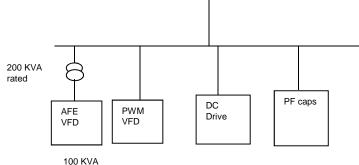


AFE Converters



Significant harmonics above 50th order

AFE Converters

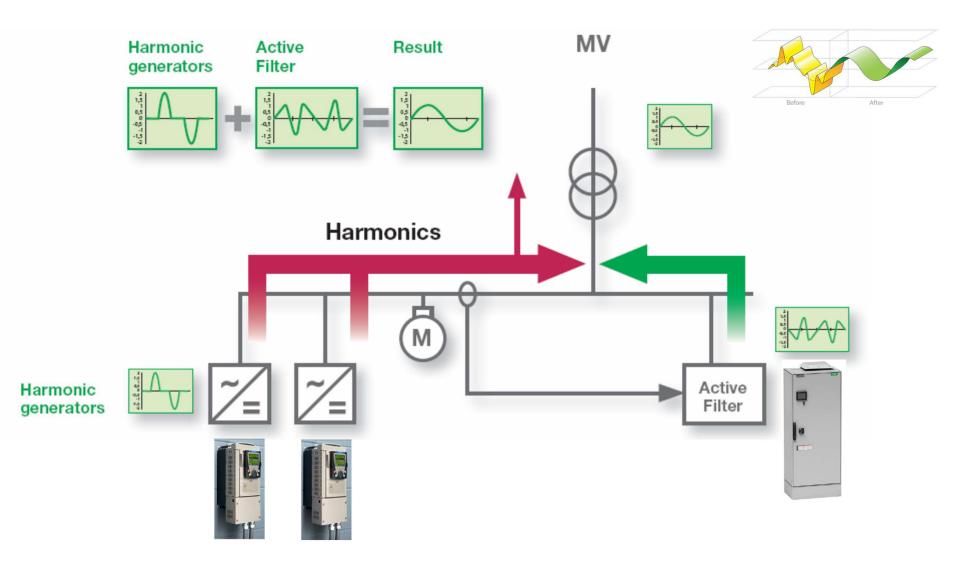




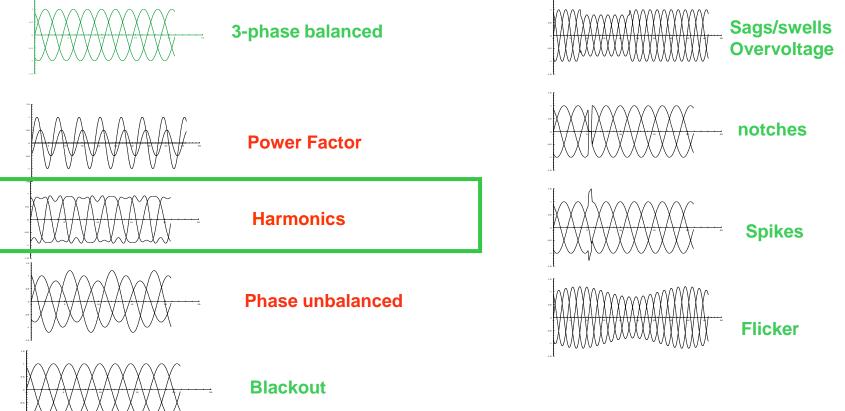
Cons

- Larger and more expensive than 6 pulse drives
 - Approximately twice the size & price
- Mains voltage must be free of imbalance and voltage harmonics
 - Generates more harmonics
- Without mains filter THD(V) can reach 40%
- Requires short circuit ratio \geq 40 at PCC
- Switched mode power supplies prohibited
- Capacitors prohibited on mains
- IGBT & SCR rectifiers prohibited on same mains
 - No other nonlinear loads permitted

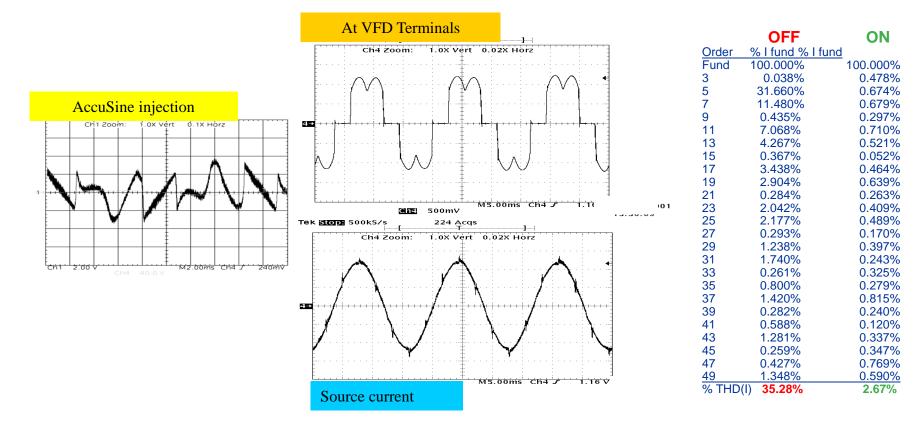
Active Harmonic Filter



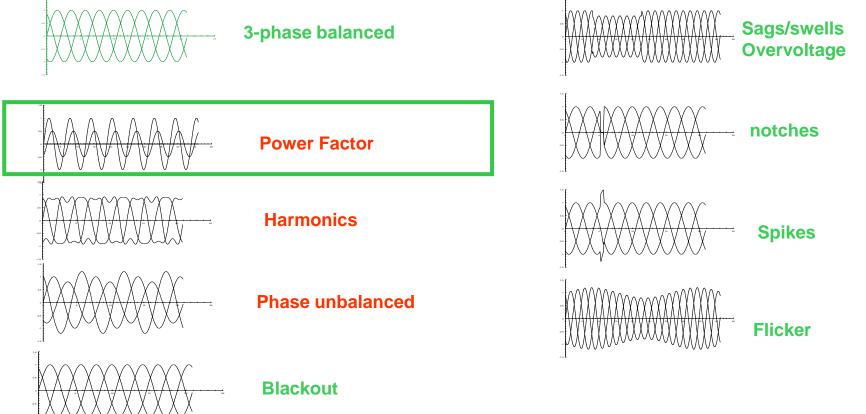
The ideal voltage supply does not exist, some AHF can correct 3 PQ problems



Harmonic Mitigation with AHF



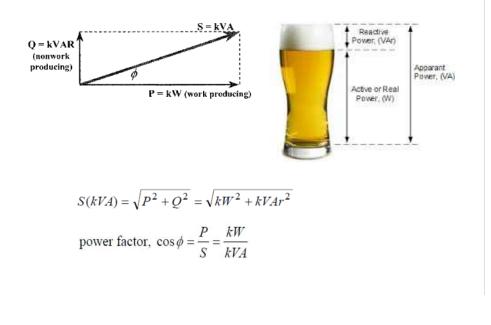
The ideal voltage supply does not exist, Active Harmonic Filters can correct 3 PQ problems



"Evolution" of Power Factors

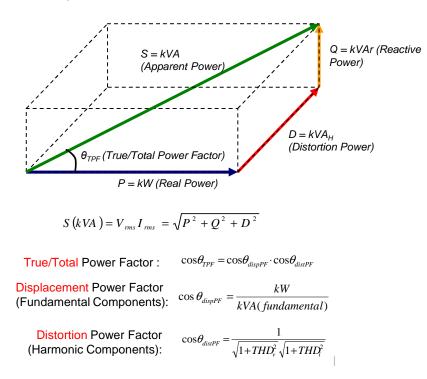
With linear vs. nonlinear loads

Electrical system with ONLY linear loads



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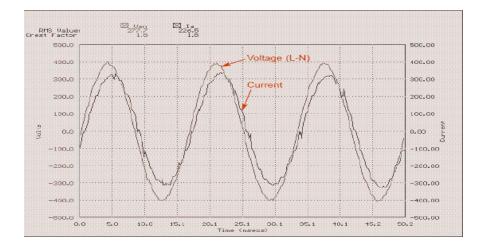
Electrical system with Nonlinear loads



Active Harmonic Filter PF correction

When PF mode is activated

- Assign priority to Harmonic or PF (fundamental) modes.
- AccuSine injects fundamental current (60 Hz) to correct the Power Factor.



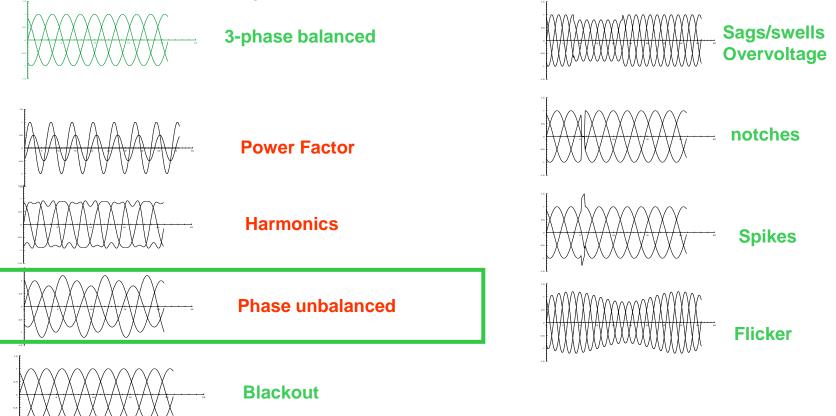
$$I_{as} = \sqrt{I_h^2 + I_f^2}$$

 I_{as} = rms output current of AccuSine PCS

- I_h = rms harmonic current
- I_f = rms fundamental current

Examples		
I _{as}	l _h	l _f
100.0	10.0	99.5
100.0	20.0	98.0
100.0	30.0	95.4
100.0	40.0	91.7
100.0	50.0	86.6
100.0	60.0	80.0
100.0	70.0	71.4
100.0	80.0	60.0
100.0	90.0	43.6
100.0	95.0	31.2

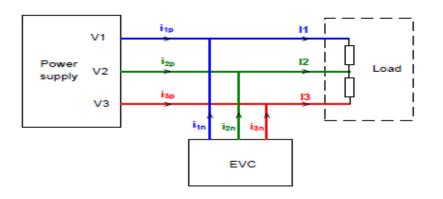
The ideal voltage supply does not exist, some AHF can correct 3 PQ problems



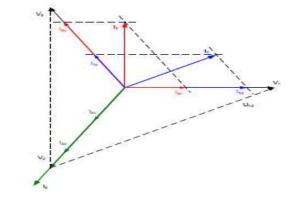
Load Balancing with some Active Harmonic Filter

Principle of load balancing

The principle of load current balancing is to inject a system of negative sequence current into the circuit (i_{1n}, i_{2n}, i_{3n}) , so that only the system of positive sequence current (i_{1p}, i_{2p}, i_{3p}) has to be generated by the power supply.



Vector construction of positive and negative sequence systems:



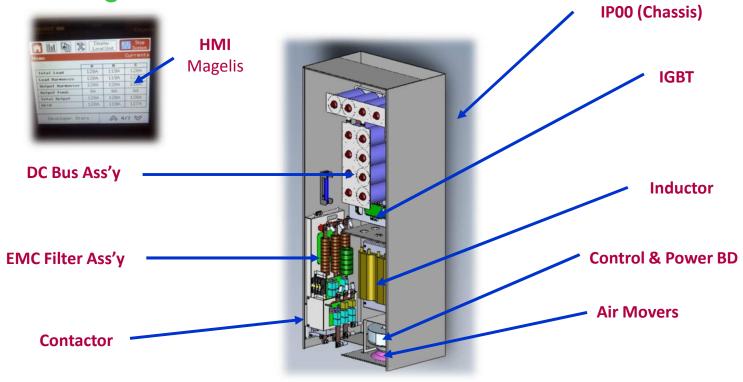
Example of Active Harmonic Filter ratings & performance



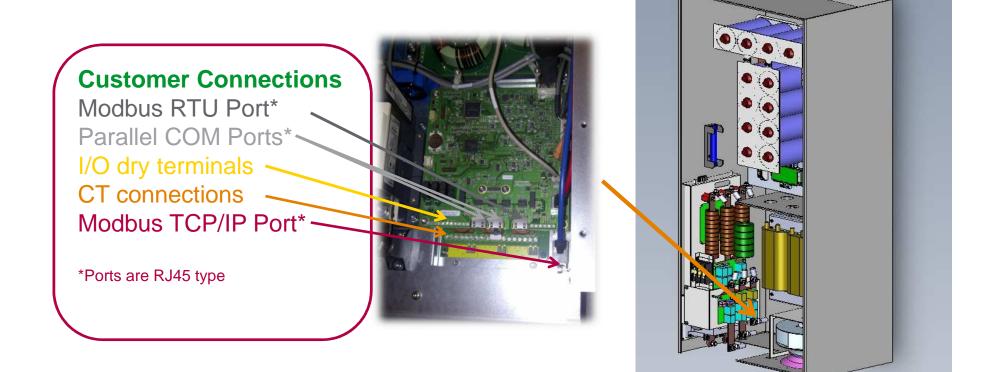
AHF ratings:

- Dynamic Harmonic mitigation form the 2nd to the 51st harmonic order
- Can meet a THD(I) of 3%, THD(V) and THD(I) target set point
- Standard Voltage, 208,240, 480, 600 and 690 V, 50-60 Hz
- Wall Mount or Free Standing, Main Lugs or Main Breaker incoming
- 60, 120, 200 and 300 A @ 480 V or 47, 94, 157 and 235 A @ 600 V per cubicle
- Enclosure type: NEMA 1, NEMA 2 and NEMA 12
- 3 levels IGBT design with optimized losses
- Closed loop c/w FFT digital logic
- 2 cycle response time for harmonic correction and ¼ of a cycle for reactive power injection
- cULus and CE certified
- And much more...

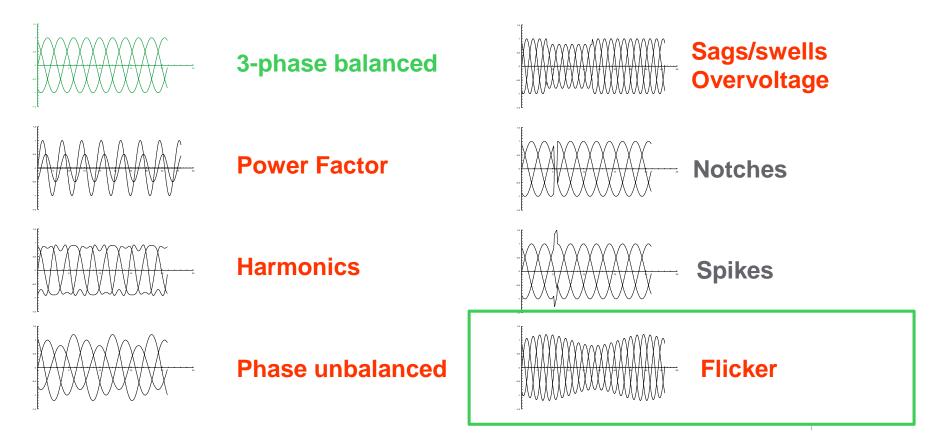
Technical Structure of Active Harmonic Filter, based on Schneider design



Technical Structure of Active Harmonic Filter, based on Schneider design



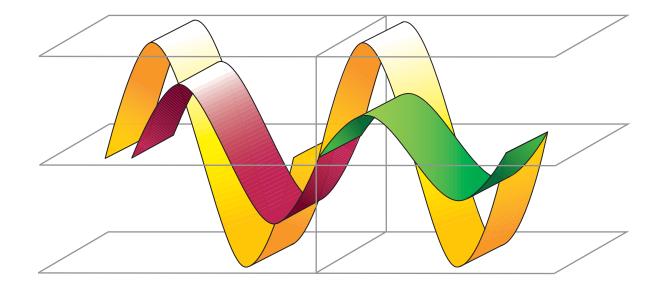
The ideal voltage supply does not exist



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Introduction to Hybrid Var Compensator (HVC)

HVC is a solution for flicker compensation



Flicker Producing Loads





Spot welder



Steel Shredder

Ball Mill (Rock Crushers)

- Large motors starting
- Hoist
- Rubber batch mixer
- Linear Induction Motor
- ...

Results in equipment faulting Results in flicker (medical issues)

Hybrid Var Compensator (HVC)

Real Time VAR injection for voltage regulation and flicker control.

- Available in Low and Medium Voltage
- High Speed Response, 5 milli seconds response time
- Infinite Variability
- Full Duration
- Can maintain Unity Power Factor



HVC Approach

Use fix or automatic capacitor bank

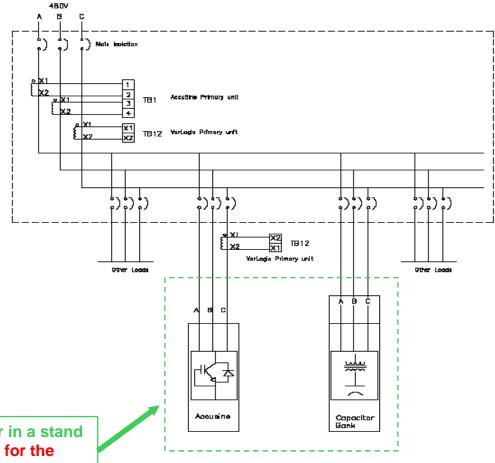
for inrush support

- Always on line
- Instant response

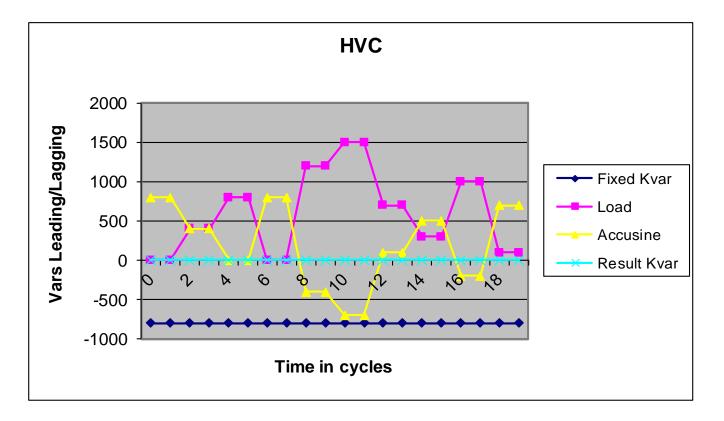
Use AccuSine PFV+ for fine tuning

- Injects leading or lagging VARs
- Cancels fix caps leading VARs at no load
- Adds leading VARs as loads increase
- 5 ms response time

HVC in one enclosure or in a stand alone cubicle. Designed for the customer's site requirement.

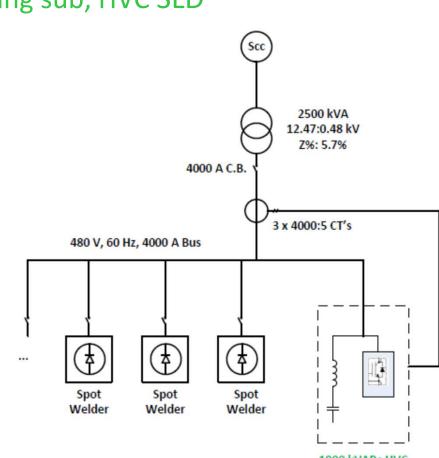


HVC Concept



LV HVC case study spot welding sub station



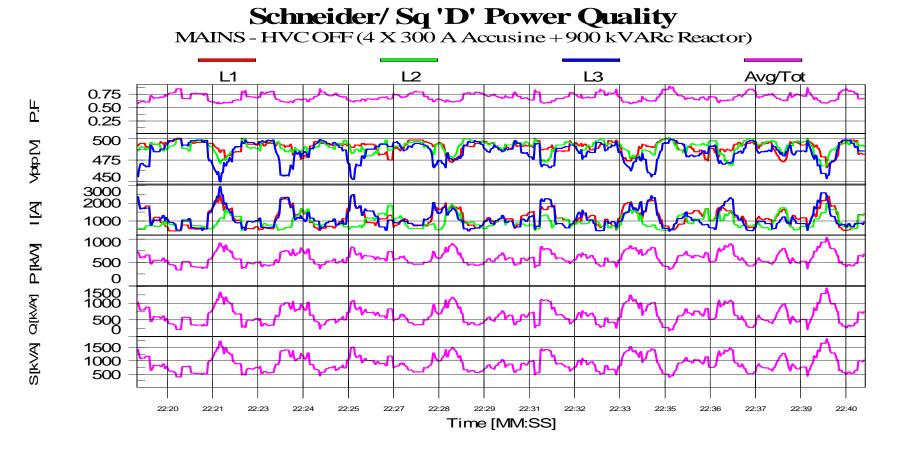




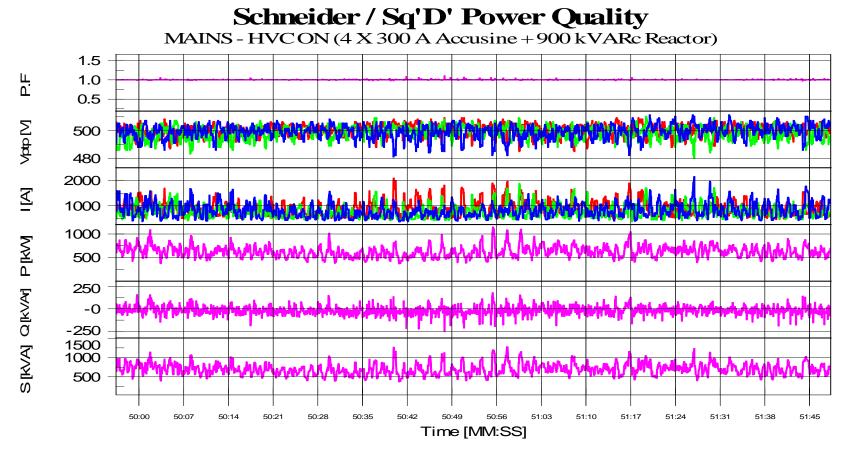
Futaba welding sub, HVC SLD



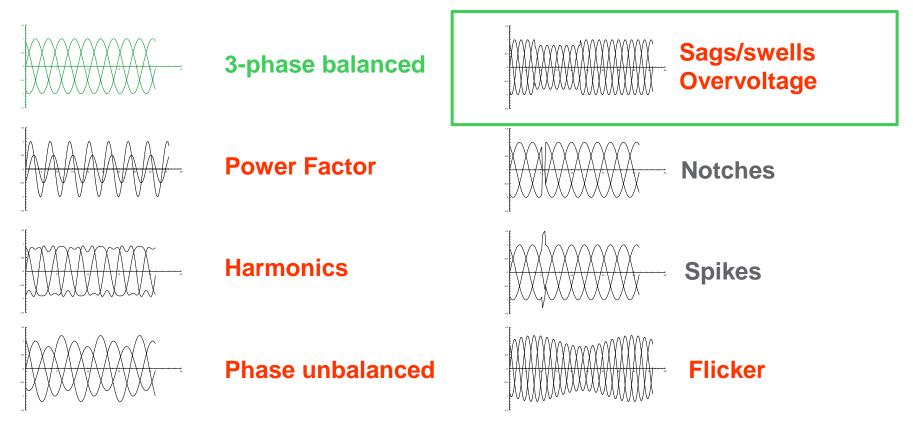
Futaba welding sub, HVC OFF



Futaba welding sub HVC ON



The ideal voltage supply does not exist



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Voltage Problems – Basics

Chronic Voltage Regulation issues

Voltage outside $\pm 10\%$ for > 60 seconds

Voltage Sag

Voltage < 90% for $\frac{1}{2}$ cycle to 1 minute

Interruption

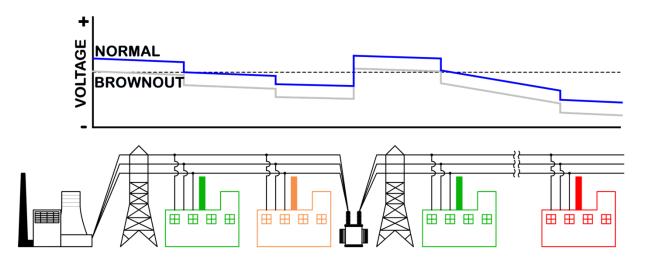
Voltage < 10% for ><u>3 cycles</u>

95% of Voltage Quality Problems





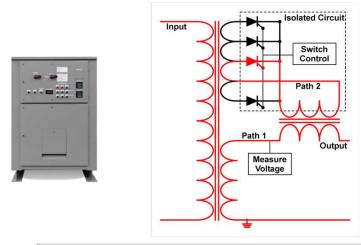
Chronic Voltage Problems

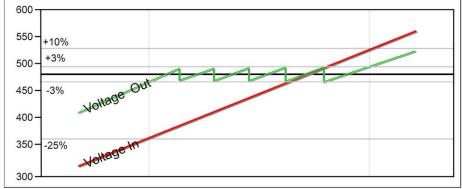


Brownout – intentional reduction in grid voltage

External: Line Drops & Brownouts

Solid State tap switching voltage regulator





- Microprocessor controlled tap-switching
- Input voltage range: +10 to -25%
- Output regulation: ±3%
- Response time: 1 cycle typical
- Overload capacity: 1000% for 1 second
- No load or power factor limitations
- Independently regulated, shielded, isolated output
- Fan-free and maintenance-free
- Single or three phase
- 5 to 2,000 kVA
- 50 or 60 Hz
- Any input or output voltages up to 600v

Voltage Problems – Basics

Chronic Voltage Problems

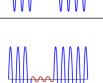
Voltage outside $\pm 10\%$ for > 60 seconds

Voltage Sag

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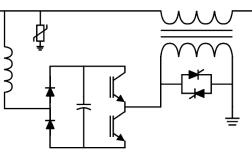
Voltage < 10% for ><u>3 cycles</u>

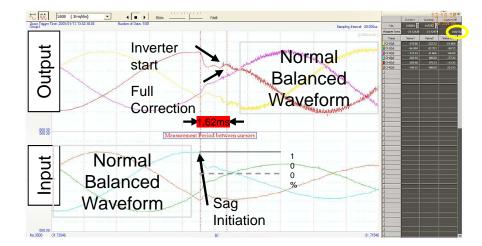


95% of Voltage Quality Problems

Sag Mitigation Device

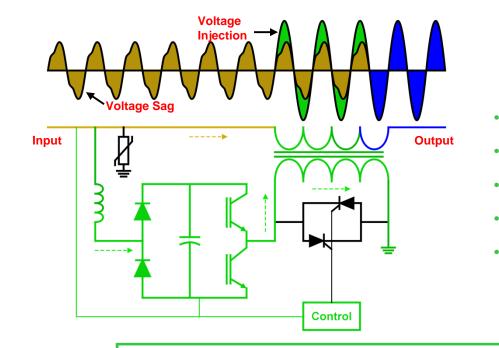






- Inverter-based, voltage injection design
- Corrects voltage sags back to >95% nominal voltage
- For sags down to 30% remaining voltage for 1 or 2 phase sags
- For sags down to 60% remaining voltage for 3 phase sags
- 2ms response time
- Provides SEMI-F47 compliant protection
- Correction independent of load or load pf, sag depth or duration
- Corrects phase shifting during sag event
- 3 phases, 20 to 2500 kVA, 50 or 60 Hz, up to 600 V.

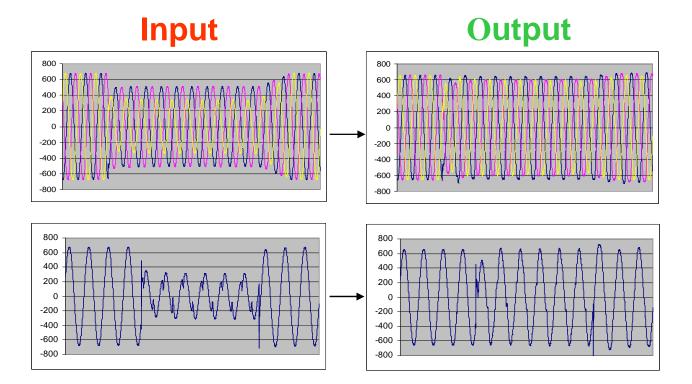
Sag Mitigation Device operation



•Transformer "un-shorted"
•DC bus powered - inverters are ON
•Energy transfer phase-to-phase
•Non-sagging → sagging phases
•All in 2 ms

Input Voltage Sag – Correction Mode

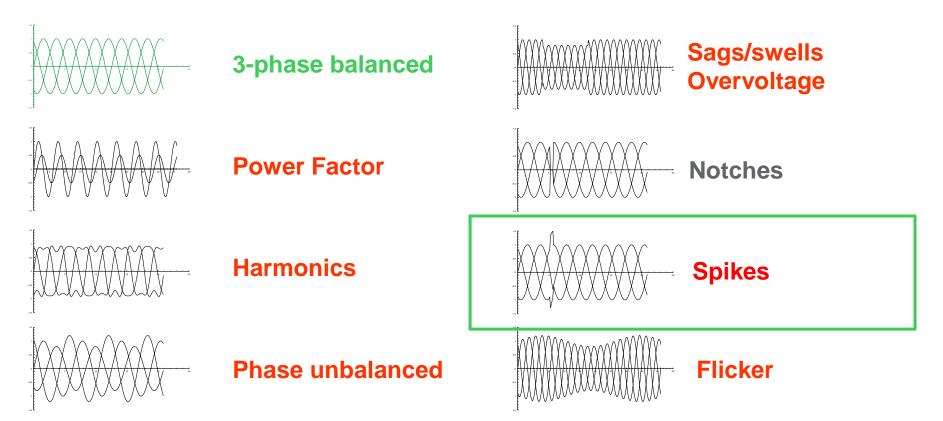
Electronic Sag Fighter Performance



Surge Suppression Device aka SPD or TVSS

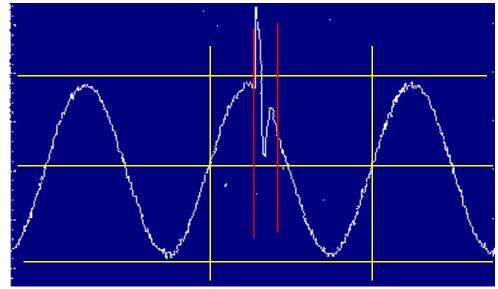


The ideal voltage supply does not exist

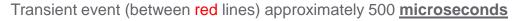


What is a Transient Surge?

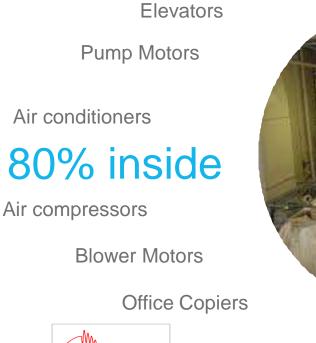
- > A surge or transient is a voltage spike that only lasts a few millionths of a second (the "blink of an eye" is thousands of times longer than the typical surge).
- > A surge can contain thousands of volts and thousands of amps.

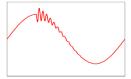


1 cycle at 60 Hz = 16.6 milliseconds



Where do Transient Surges Come From?





Oscillatory transients Typical of Internal events (Smaller, lower energy) Lightning

Electrical Accidents

20% outside

Switching Cap Banks

Utility Grid Switching

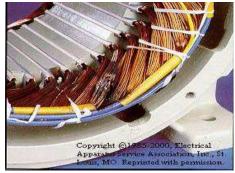
Impulse transients Typical of External events (Larger, higher energy)



The Effects of Transients on Business

\$80B





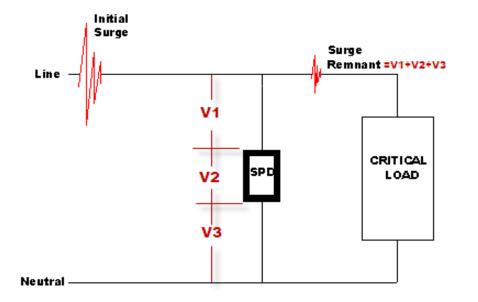
"Power related problems cost companies over \$80 billion a year" (Source: Lawrence Berkeley National Laboratory)

> "Industry experts estimate that power surges cost businesses \$26 billion annually in lost time and equipment repairs and replacements" (Source: Insurance Institute for Business & Home Safety)

- High Facility Maintenance Costs
- Equipment Failure
- Long-term System Degradation
- Process disruptions
- Data Loss or Corruption
- Costly Downtime
- Safety to drinking water

How does an SPD work?

The purpose behind installing any SPD is to divert damaging voltage and currents away from down stream equipment.

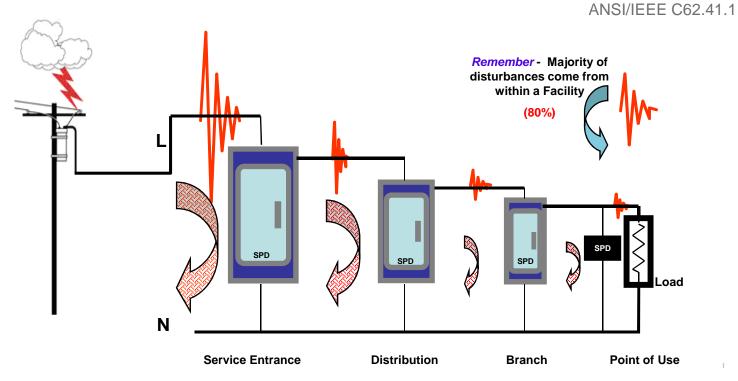


SPD Method of Operation

- 1. The SPD is installed in a parallel path with respect to the load.
- 2. When a surge voltage does come down the line, the SPD will respond in nanoseconds creating a low impedance path through the components within the SPD.
- 3. Current will flow through the path of least resistance.
- 4. The SPD becomes the path of least resistance and shunts the damaging energy before it is forced through the down stream protected equipment.
- 5. NO SPD will shunt 100% of the initial surge energy away from the load.
- 6. There is always some voltage/current that will be pushed through the load.
- 7. But if the SPDs are installed correctly and in the proper location, this Surge Remnant is well below the damaging level of the protected equipment.

Cascading - Location

Cascading (layering) your protection throughout your facility insures proper protection for your equipment.



Recommended Protection Levels

kA ratings may be modified to correspond with the intensity and frequency of transients.

High lightning areas or areas with a high frequency of Utility grid switching surges, etc. may require an increased kA rating.

The higher the frequency of surges, the higher the kA rating should be.

The dollar value of sensitive electronic equipment which receives power from the Branch Panel needs to be considered.

```
Service Entrance 240 kA
Distribution 120-160 kA
Branch 100-120 kA
```

Rule of Thumb: The higher the kA rating on the product, the better its withstand capability and overall robustness. For each level of protection (cascading) cut the kA rating by half.

Modes of Suppression: L1-N, L2-N, L3-N, L1-G, L2-G, L3-G, L1-L2, L1-L3, L2-L3, N-G.

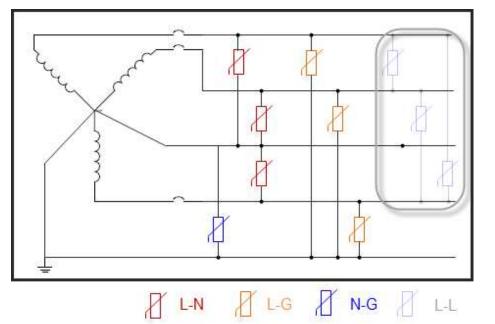
<u>4 Mode</u>: 4 Mode will provide protection to all ten modes via the L-N and N-G MOV's.

- This configuration is not used often because of the poor levels of protection but is sometimes used because of the costs.

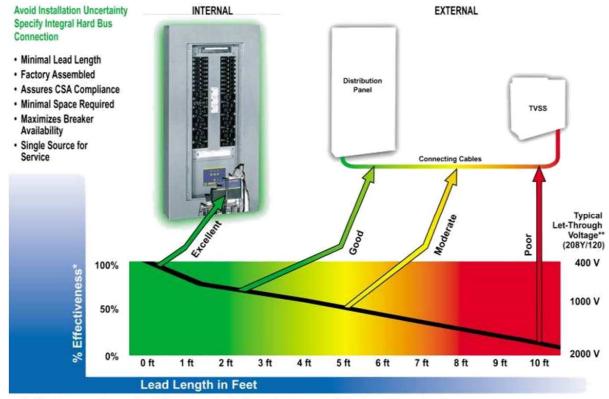
<u>7 Mode:</u> The 7 Mode will provide protection to all ten modes via the L-N, L-G and N-G MOVs.

- This is the most popular configuration as it allows the components to properly be sized for the voltage levels they are intended to protect. Most Manufactures, provide this type of surge protection, it is proven and works well.

<u>10 Mode:</u> 10 mode, All Mode and Discrete, are more Marketing driven via a Specification than performance driven.



Lead Lengths - Installation



* % Effectiveness refers to the ability to keep the let-through voltage at minimal levels for typical transients ** Approx 160V / ft

Transient Voltage Surge Suppressors









	Power Factor
MMM	Harmonics
	Phase unbalance
	Blackout
	Sags/swells Overvoltage
	Notches
	Transient (Spike)
	Flicker
	Noise

Case Studies

Active Harmonic Filter, turnkey project



Problem:

In late 2013 the hospital reported the **malfunction of several dialysis** machines which were significantly impacting the quality of patient care.

Audit:

Audit **PME 7.2** (Power Monitoring Expert) was utilized, in conjunction with a fleet of PM800 Meters to gather the Power Quality Data.

The existing situation The hospital's air conditioning system had recently been modernized with a fleet of Variable Speed Drives (VSDs). These VSDs were polluting the hospital's electrical network and were regularly causing the new dialysis machines to malfunction. Diabetic patients were regularly sent home and asked to reschedule their treatment.

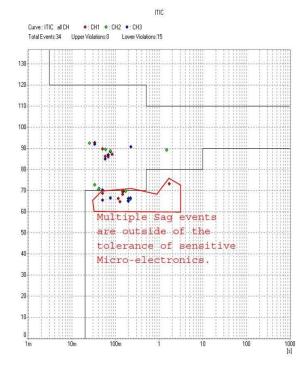
• The VSD caused high THD(V) and THD(I)

Other PQ issues:

- Switching from Utility to generator mode caused short duration voltage sags
- Utility Capacitor bank switching caused ringing transient and multiple Zero Crossing

Power Quality disturbances

- 1. Voltage Distortion (aka THDv or Vthd)
- 2. Voltage Sag
- 3. Multiple Zero crossings



POWEI Freq	R		7.813 Hz	V	DLTAGE			CURRE	NT	
P1			0473MW	TF	1	116.48	v	11		0.
P2			0611MW			115.84			0.	
P3		0.	0521MW	U	3	115.70				0.
Psum S1		0.	1606MW 0497MVA 0619MVA	TI	HD-U1	3.25	%	THD-11		
51 52		0.0	1437MVA	T	4D-U2	7 41	2	THD-12 THD-13		
\$3		0.0	0537MVA	U	pk+1	157.57	'V	lpk+1		0
Ssum		0.1	653MVA	U	pk+2	163.26	v	lpk+2		0
Q1)152Mvar)097Mvar			165.17 -164.25	<u>×</u>	lpk+3		0
Q2 Q3)128Mvar			-164.25				- 0
Qsum			377Mvar	U	pk-3	-159.81				- 0
PF1			0.9523	U.	ave	116.01		KF1		
PF2			0.9877	U	unb	0.65	%	KF2		
PF3 PFsum			0.9711	-			-	KF3 lave		0.
rsum			0.5110					lunb		0.
	POWER			Ĩ	VOLTAGE		сц	RRENT		
	Freq		59.968	_	. SERIOL					
	P1		0.0728M		111	119.09 V	[1		0	6225kA
	P2									
			0.0872M			120.17 V				7303kA
	P3		0.0717M	-		120.51 V	-		0.	6059kA
	Psum		0.2317M			4.78 %				7.94 %
	S1		0.0741M	VA	THD-U2	4.72 %	TH	D-12		8.86 %
	S2		0.0878M	VA	THD-U3	4.70 %	TH	D-13		8.51 %
	\$3		0.0730M	_		163.45 V	_		ſ	0.931kA
	Ssum		0.2349M			163.81 V				1.112kA
			0.0139M			164.07 V				
	Q1									0.939kA
	Q2		0.0103M			-162.38 V				0.944kA
	Q3		0.0137M			-162.93 V				1.120kA
	Qsum		0.0379M	var	Upk-3	-163.74 V	lpk	-3	- ().940kA
	PF1		0.98	24	Uave	119.92 V	KF	1		1.53
	PF2		0.99	30	Uunb	0.30 %	KF:	2		1.47
	PF3		0.98	22		11000000 VI	KF:	3		1.59
	PFsum		0.98				lav		0	6529kA
	T T SUIT		0.00	55			lun		0.	6.18 %
										0.10 %
	CH1-3: 20	0.00 V	OLTAGE/CUP	RREI	CH2 CH2	No.48 01/21 08:18 H3	53.1	50 Wave CH	11]	
		\mathbf{i}	\frown	/\	VV	\searrow	X	\frown		V
		/	\bigvee		$\int $	$/ \setminus /$		\bigvee		\bigwedge
			/	J	M^	\square		\bigwedge	Λ	
	CH1-3: 0.5	30001-2	vdiv — c	HI	CH2	CH3				
				N		\searrow	X	\frown	1	~
	K	>	$\langle \rangle$	7		$ \land \land$		25	2	
	5	2	X				-	X	X	
	5	>	X			\bigcirc	-	X	X	Ź

X

Turnkey Solutions: Engineer, Supply, Install & Start up

High THD(V) & THD(I)

Supply:

- 2 x 94 Ampere AccuSine PLUS at 600 V
- 2 x 157 Ampere AccuSine PLUS at 600 V
- 24 x Current Transformer
- 78 x 3% linear reactors for the VSD's

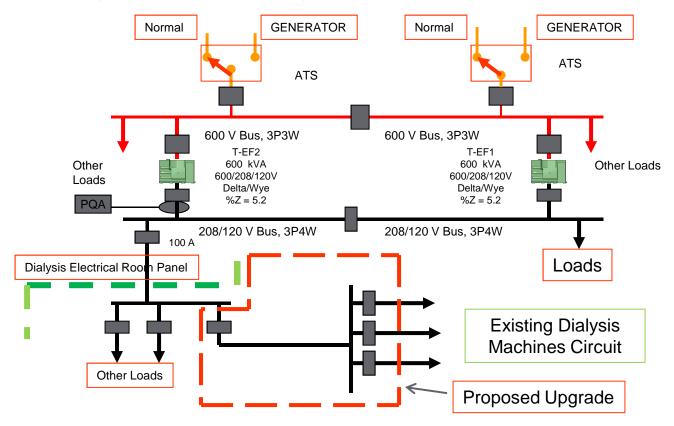
Voltage Sags

• <u>Not coincidental with dialysis machines shut down</u>, not considered to be a problem by the end user.

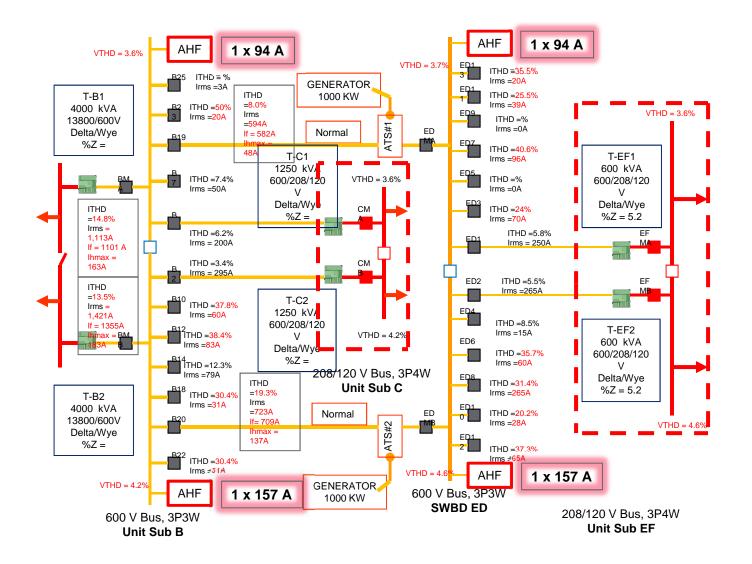
Ringing Transient and Multiple Zero Crossing

 Installation of 3 isolation transformers on branch circuits that are feeding sensitive equipment

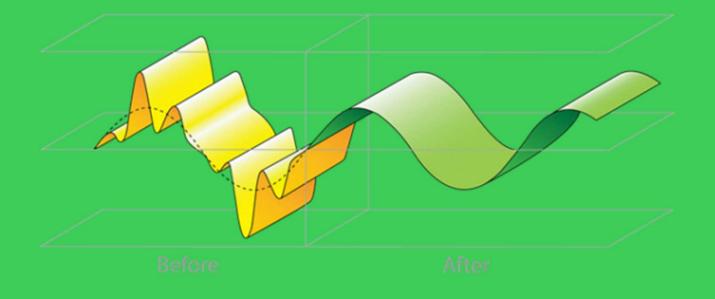
Simplified hospital electrical system



T-EF2/ T-EF1 Electrical System



Low Harmonic Emission Solution



2 ways to achieve 'Low Harmonic' system

Main Goal

• Comply with harmonic standards and reach a THDi level below 5%

2 solutions

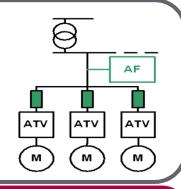
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Active Harmonic Filters for multiple standard VSD

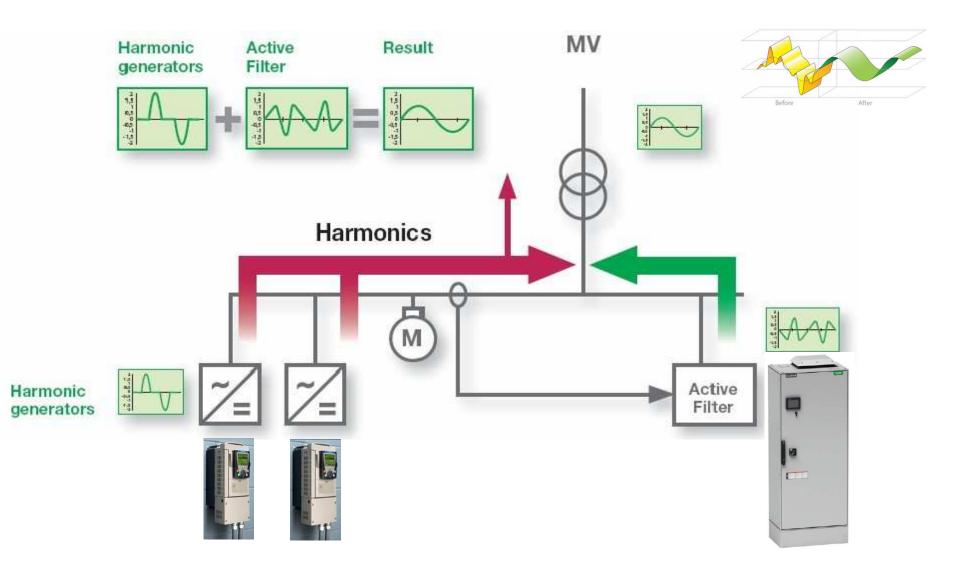
- Active Harmonic Filter (AccuSine PCS+ 60,120,200 or 300A) :
 - For groups of multiple ATV600 & ATV900, up to 630kw each
 - Achieve a THDi below 5%
 - AC or DC chokes are needed at VSDs level (3-5% Z) to meet 5% THDi
 - Can also be used to compensate for harmonics from non-VSD loads on the same bus as well as to provide PFC for line connected motors

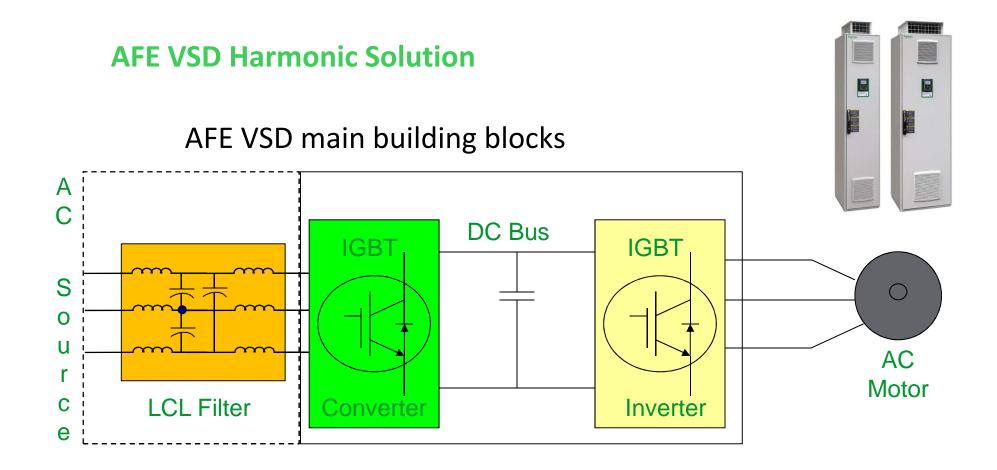


- One enclosure with ATV680 & ATV980, complete with AFE module
- 380 to 480 V, 50/60Hz, IP23 & IP54, THDi < 5%
- •Can achieve a PF of 100%



End-User < 5% THDi





AFE Drive advantages

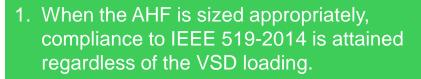


- 1. It's normally more cost effective for application with one large drive in comparison to AHF.
- 2. It has a foot print advantage over the AHF for installation of one or two drives.
- 3. It will be compliant to IEEE 519 when operating as Low Harmonic Drive when transmitting full load power.
- 4. It has a high power factor, going as high as 99% lagging, for most application it will have a lower kVA demand than AHF combined with 6-pulse VSD.
- 5. It's capable of re-injecting power into the grid during dynamic breaking, therefore yielding some operation expenses saving during these instances.

AHF PCS+ advantages when Combined with standard 6-pulse VSD

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- 2. One AHF can correct for multiple 6-pulse VSD unlike an AFE VSD where you need one for each drive product, making AHF more cost effective for multiple drive application, especially when redundant pumps are present.
- 3. AHF have less losses compared to AFE Drive, therefore it reduces the installation operating cost over time.
- 4. AHF introduces less switching ripple than AFE because it uses a higher commuting frequency, therefore reducing the risk of interaction with other loads present in the network.
- 5. AHF can simultaneously correct PF and do load balancing while mitigating harmonic, therefore improving the overall power quality of the installation.

AHF PCS+ advantages when Combined with standard 6-pulse VSD



- 6. The AHF parallel installation makes it easy to retrofit an installation and it also increases the continuity of service, basically the drive can still operate even though the AHF is off line.
- 7. The AHF can easily be integrated in MCC or in switch gear which can optimize the installation footprint and reduce construction costs.
- 8. Generally speaking, 6-pulse VSD are more robust and less complex than AFE VSD, therefore reducing maintenance frequency and complexity when they are combined with AHF.
- 9. AFE VSD offer begins at 110 KW and increases with KW rating. AHF advantage is that it be applied to all KW ratings (from 0.75 KW to 900 KW).

ATV680 Versus ATV630/ATV660 combined with PCS PLUS 1 to 5 identical VSD ranging from 110 to 500 kW

Base of comparison:

- the cost of ownership,
- the lineup footprint,
- the lineup weight,

Green color: advantage to the Altivar 680

- the apparent power
- and the losses of each solution.

Blue color: advantage ATV630/ATV660 + AccuSine PCS+

	Quanti	ity of 1 VFD	on the sar	ne bus		Quantity of 2 VFD's on the same bus						Quantity of 3 VFD's on the same bus							Quantit	ty of 4 VFD	s on the sa	me bus	Quantity of 5 VFD's on the same bus						
Advantag	ge ATV680	Advanta	age ATV630	/ATV660 w	th PCS+	Advanta	ge ATV680	Advantage ATV630/ATV660 with PCS+			Advantage ATV680 Advantage ATV630/ATV660 with PCS PLUS			Advantage ATV680 Advantage ATV630/ATV660 with PCS PLUS			PCS PLUS	Advanta	age ATV680	Advantage	ATV630/A	ATV660 with	PCS PLUS						
VFD size	Cost of	Line up	Weight	Adj. KVA	Losses	VFD size	Cost of	Line up	Weight		Losses	VFD size	Cost of	Line up	Waight	Adj. KVA	Losses	VFD size	Cost of	Line up	Waight	Adj. KVA	Losses	VFD size	Cost of	Line up	Waight	Adj. KVA	Losses
in KW	ownership	length	weight	AOJ. KVA	(kw)	in KW	ownership	length	weight	Weight Adj. KVA	(kw)	in KW	ownership	length	weight	AUJ. NVA	(kw)	in KW	ownership	length	weight	AUJ. NVA	(kw)	in KW	ownership	length	weight	AUJ. NVA	(kw)
110						110						110						110						110					
132						132						132						132						132					
160						160						160						160						160					
200						200						200						200						200					
250			1			250						250						250						250					
315						315						315						315						315					
355						355						355						355						355					
400						400						400						400						400					
450						450						450						450						450					
500						500						500						500						500					

Conclusion







Both types of systems are effective at mitigating harmonic distortion and meeting the most stringent harmonic standard around the world.

An advantage can be given to AFE VSD when only one drive is involved in the project, especially when dynamic braking is required.

However, a definitive advantage can be given to AHF with standard 6-pulse VSD when multiple drives are operating on the same bus. The greatest benefits of a system solution are reflected in the cost of ownership and the operating cost reduction due to the efficiency optimization of a system approach . Questions?